

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masaru FUKU, et al.

Application No. To be assigned

Filed: October 18, 1999

For: ONBOARD SEMICONDUCTOR DEVICE

Group Art Unit: To be assigned

Examiner: To be assigned



## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §§ 1.97 and 1.98

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to Applicants' duty of voluntary disclosure under Rule 56, and for the Examiner's convenience, we submit herewith:

- a) a form PTO-1449 listing one (1) reference of potential relevancy; and,
- b) a complete copy of the reference.

No certification or fee is required.

Regarding the concise explanation of relevancy requirement for foreign language documents, an English language summary is enclosed for the reference.

Respectfully submitted,

Alan J. Kasper

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Date: October 18, 1999